Tunable electrical memory characteristics by the morphology of self-assembled block copolymers:PCBM nanocomposite films†

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Resistive switchable memory devices were fabricated using self-assembled composite thin films of asymmetric poly(styrene-block-4-vinylpyridine) (PS-b-P4VP) block copolymers (BCP) and fullerene derivatives (PCBM). L1 (with a longer PS block) was comprised of discrete vertical P4VP nanocylinders embedded within the PS matrix whereas L2 (with a longer P4VP block) revealed a reverse morphology with a horizontal orientation. They were used to control the spatial location or distribution of the PCBM and the resultant memory characteristics. The devices with ITO/BCP:PCBM/Al configurations exhibited variable multi-electronic characteristics, changing from insulating to bistable memory switching and highly conducting, as the PCBM content increased. The L1:PCBM memory device showed non-volatile write-once-read-many-times (WORM) memory behavior but the L2:PCBM device exhibited a volatile nature of static random access memory (SRAM). Both L1 and L2:PCBM composite devices revealed the improved switching performance upon solvent annealing procedures of the composite thin film. Our results suggested that the controlled morphology of the BCP/PCBM composite could create nanoscale charge-storage elements for a high density memory device with a reduced bit cell size.

1 Introduction

Resistive switching materials made of organic polymer thin films have been recognized to be promising alternatives to their traditional inorganic counterparts due to the advantages of structural flexibility, low-cost, solution processability, and three-dimensional stacking capabilities. The active layer exhibits distinct high and low conductance responses to applied voltages for application as digital memory devices with simple sandwiched structures. The reported polymer-based systems for such applications included conjugated polymers, functional polymers (PIs), polymers with pendent electroactive chromophores, and polymeric composites. The memory switching behaviors of composite materials using various charge-storage filled additives (metal or fullerene derivatives) in the supporting matrices were identified as being achieved via several proposed mechanisms, such as charge trapping/detrapping, charge-transfer effect, filament formation, and polarization effect. When the recording domains are down to the nanoscale, new materials and advanced techniques must be explored to satisfy the need of storage capacity. However, the issues of non-homogenous large-size aggregated fillers within an active layer make it less possible to meet the requirements of scalable fabrication for the growing demand on high-density memory arrays of nanoscopic element cells. Block copolymers (BCP) can self-assemble into various ordered nanostructures (such as lamellae, cylinders, spheres and so on) with length scales ranging from 10 to 100 nm due to the repulsion between the chemically connected blocks. Recently, many efforts have been dedicated to achieving the spatial and orientational distribution of the nanoelements within the ordered scaffolds offered by BCP, because the cooperative self-assembly in polymeric hybrids offers rich possibilities to develop novel electrical properties. The diverse nanodomains of BCP bulk species can be achieved by tailoring the nature of BCP (chemical structures, the length of each block, and architecture) and micellar nanostructures of BCP films can be obtained by altering the polymer concentration, choice of solvent or processing conditions. Among the diverse nanostructures mentioned above, the spherical micelles of BCP were often selected as structural components for control over the functional clustering size and selective spatial location of filled additives in order to finely tune and optimize the memory properties. Leong et al first demonstrated the versatility of using...
self-assembled BCP with the in situ synthesis of Au nanoparticles (NPs) for polymer memory device fabrication, in which the capacitance–voltage (C–V) or current–voltage (I–V) hysteresis in the capacitor or transistor structure represented charge confinement in the embedded Au NPs.\(^{34–36}\) Besides the ordered cylindrical nanostructures of BCP being used to selectively sequester the Au NPs, they also exhibited resistive switching.\(^{21}\) The distribution of charge-storage sites within the thin memory film layer indeed determines the memory device performance. Recently, we demonstrated a supramolecular approach to prepare controlled domain sizes of poly(fluorenylstyrene)-block-poly-(2-vinylpyridine) P(StFl-b-P2VP):PCBM composite thin films for use in a non-volatile memory device.\(^{28}\) However, the PCBM domain in the P(StFl-b-P2VP) matrix was disordered and non-oriented. Thus, it would be of significant interest to explore the correlation between resistive switching of polymer composites and their self-assembled spatial ordered morphologies systematically.

In this paper, we report the morphology, properties, and memory device characteristics of self-assembled asymmetric poly (styrene-block-4-vinylpyridine) (PS-b-P4VP) BCP and their hybrids with different amounts of PCBM. This composite system served as an excellent archetype; the self-assembled BCP generated the nanotemplate to selectively control the spatial locations of the PCBM molecules as the electronic traps, in which the nitrogen atom in the P4VP block provided the physical stabilization for achieving uniform PCBM distribution. The chain length of each block and solvent annealing conditions could manipulate the PCBM aggregated nanostructures in the continuous insulating matrix that created tunneling barriers for charge-transport. Atomic force microscopy (AFM), transmission electron microscopy (TEM), and grazing-incidence small angle X-ray scattering (GISAXS) were performed to characterize the morphology of the BCP composite thin films. The memory device with the configuration of ITO/BCP composite/AI was employed to explore the energy levels of the memory characteristics that were further analyzed by the appropriate conduction model. The self-organized PCBM approach created the nanotrapping sites obtained through the BCP morphology that determined the memory characteristics.

1 Materials

Two asymmetric linear poly(styrene-block-4-vinylpyridine) (PS-b-P4VP) diblock copolymers were purchased from Polymer Source Inc. (Dorval, Canada) which were named L1 (\(M_n^{PS} = 48.0\) kg mol\(^{-1}\), \(M_n^{P4VP} = 20.3\) kg mol\(^{-1}\), \(M_d/M_n = 1.13\)) and L2 (\(M_n^{PS} = 17\) kg mol\(^{-1}\), \(M_n^{P4VP} = 49\) kg mol\(^{-1}\), \(M_d/M_n = 1.15\)), respectively. The volume fraction of the PS blocks (\(\Phi^{PS}\)) of L1 and L2 are 0.70 and 0.26, respectively. \([6,6]\)-Phenyl-C\(_{61}\)-butyric acid methyl ester (PCBM) was obtained from Nano-C Inc. Chloroform and methanol were purchased from Acros Organics (Geel, Belgium). All the chemicals were used as received and without further purification.

1.2 Characterization

The UV-Vis spectra were obtained from the solution-state with a Hitachi U4100 UV-Vis-NIR spectrophotometer at room temperature. Electrochemistry was performed with a CHI 611B electrochemical analyzer. Cyclic voltammetry (CV) was performed with the use of a three-electrode cell in which ITO was used as the working electrode and platinum wire was used as the auxiliary electrode. All cell potentials were taken with the use of a homemade Ag/AgCl, KCl (sat.) reference electrode. The thickness of the polymer film was measured with a Microfigure Measuring Instrument (Surfcomer ET3000, Kosaka Laboratory Ltd).

Atomic force microscopy (AFM) measurements were obtained with a NanoScope IIIa AFM (Digital Instruments, Santa Barbara, CA) at room temperature. Commercial silicon cantilevers (Nanosensors, Germany) with typical spring constants of \(21–78\) N m\(^{-1}\) were used to operate the transmission electron microscope (TEM). The TEM images were obtained using a JEOL JEM-1230 TEM instrument operating at a voltage of 50 kV with a Gatan dual vision CCD camera. The samples for TEM measurements were prepared by being spin-coated onto a NaCl substrate, and put into deionized water. The polymer composite films floated on the surface of the water and were placed onto 200-mesh copper grids. Small-angle X-ray scattering patterns were collected by X-ray scattering in a grazing-incidence geometry (Nano-Viewer, Rigaku) that includes a two-dimensional (2D) area detector (Rigaku, 100K PILATUS). The instrument is equipped with a 31 kW mm\(^{-2}\) generator (rotating anode X-ray source with a Cu K\(_{α}\) radiation of \(λ = 0.154\) nm). The scattering vector, \(q = (4π/λ)\sinθ\), along with the scattering angles \(θ\) in these patterns were calibrated using silver behenate. The thin films were mounted on a z-axis goniometer. The angle of incidence of each X-ray beam was \(α = 0.2°\), which is between the critical angles of the PS-b-P4VP films and the silicon substrates (\(α_{c1}\) and \(α_{c2}\)).

2 Experimental section

2.1 Materials

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2.2 Characterization

The UV-Vis spectra were obtained from the solution-state with a Hitachi U4100 UV-Vis-NIR spectrophotometer at room
analyzer equipped with a Keithley 4205-PG2 arbitrary waveform pulse generator. All of the electronic measurements were performed in a N$_2$-filled glove box.

3 Results and discussion

3.1 Optical properties

Upon aging, the solutions of the PS-$b$-P4VP:PCBM mixtures displayed a brown color in Fig. S1 (ESI†). Fig. 1 shows the optical absorption spectra of the PCBM:L1 and L2 mixtures in chloroform. As the PCBM content is increased from 0% to 5% and finally to 10%, the absorption intensity increases in the range of 450–550 nm. It implies the presence of induced supramolecular charge-transfer interactions between the PCBM and pyridine units of the BCP, which is similar to that reported in the literature. The optical band gaps of L1 and L2 estimated from the absorption edges are 4.49 and 4.51 eV, respectively.

3.2 Thin film morphologies

The thin film morphologies of BCP are profoundly influenced by various parameters, such as the block ratio, the strength of the interactions at the substrate interface, and surface fields of variable strength at a free surface. Here, BCP can be viewed as a template; therefore, the dispersion of the fullerene derivatives in the microphase-separated morphologies is controlled by the physical interactions after mixing. The asymmetric PS-$b$-P4VP BCP (L1 and L2) and PCBM loading ratio need to be considered to determine the morphological effect of the hydrophilic substrate which is analyzed by AFM and TEM. Fig. S3 and S4 in the ESI† show the AFM topographies and TEM images of the PCBM:L1 or L2 as-spun films with various compositions (0%, 5%, and 10%). The L1 pristine film displays a disordered dimple-type micellar structure with polar P4VP as the core and non-polar PS as the corona which is due to the chloroform solvent being slightly selective, as it is good for PS but slightly poor for P4VP ($\chi_{PS-chloroform}: 0.35; \chi_{P4VP-chloroform}: 0.67$).\textsuperscript{48,49} However, as PCBM is added into the polymer film, the dimple-like nanostructure clearly transforms into a more anomalous spherical shape for the PCBM:L1 thin film. By comparison, the spin-coated L2 thin film without PCBM exhibits disordered worm-like nanodomains. The aggregated dark phase has mainly arisen from the P4VP domain with a longer chain length. The worm-like PCBM:L2 micelles are carefully identified by TEM, in which the selective distribution of PCBM within the P4VP domain enhances the contrast between the PS and P4VP blocks due to the high electron density of PCBM. The TEM image of the PCBM:L2 thin film exhibits more distorted micelles. The kinetically-driven disordered aggregated PCBM dispersion near the P4VP cores obtained upon fast evaporation of the solvent are not thermodynamically stable and thus could not obtain a stable morphology in sufficient time.

To achieve long-range well-ordered composite nanostructures, fresh monolayered micelle films were further annealed under the saturated vapor of an appropriate solvent.\textsuperscript{40–46} As expected, a remarkable variation in morphologies resulting from the enhancement on the development of the nanodomains with a vertical orientation (L1) or parallel orientation (L2) was observed.\textsuperscript{41,45,46} Fig. 2 and Fig. 3 show the morphologies in thin films obtained from the PCBM:L1 and L2 composites under chloroform and methanol annealing, respectively, where the PCBM loading ratios range from 0% to 5% and 10%. The PCBM:L1 thin films after chloroform vapor-annealing assemble...
increase remarkably with the content of PCBM. At 10% PCBM, AFM topographies and TEM micrographs are reasonably promising. The separation of BCP reduces the PCBM aggregation size. Both AFM topographies and TEM micrographs confirm this. PCBM fills the core of the cylinder (Fig. 2) since P4VP chains can attract PCBM with the solvent. The PCBM molecules can be easily revealed to form large clusters within the continuous PS matrix. It makes them self-organize to a stable cylindrical structure with a swollen P4VP corona. Thus, the PCBM:PS–methanol: 2.36; X_P4VP–methanol: 1.26) cylinders prefer to align parallel to the substrates due to the strong affinity between the P4VP chains and the substrate surface with high surface tension (Fig. 3). The strong interaction of PCBM with polar P4VP blocks makes PCBM tend to disperse in the continuous P4VP corona domains. The fused cylindrical structures can be maintained even with a higher PCBM loading ratio. The retransformed phenomenon indicates that the solvent vapor annealing approach is successfully employed in the BCP/fullerene composite.

Under the solvent annealing conditions, the self-assembled thin film reaches an equilibrium state, in which the pristine state of L1 is comprised of discrete P4VP nanocylinders embedded within the continuous PS matrix, whereas that of L2 reveals a reverse morphology (i.e., PS nanocylinders are embedded within the P4VP domain). Thus, electrical information access of the well-defined storage sites could be performed on the nanoscale.

### 3.3 Grazing-incident small angle X-ray scattering

To acquire further insights into the morphologies of the thin films, we performed GISAXS measurements on the thin films at an incident angle of 0.2°. In-plane scan cuts were implemented on the 2D GISAXS patterns to show the 1D scattering profiles. Fig. 4(a) reveals in-plane 1D GISAXS profiles of L1 and PCBM:PS–methanol; 2.36; X_P4VP–methanol: 1.26) cylinders prefer to align parallel to the substrates due to the strong affinity between the P4VP chains and the substrate surface with high surface tension (Fig. 3). The strong interaction of PCBM with polar P4VP blocks makes PCBM tend to disperse in the continuous P4VP corona domains. The fused cylindrical structures can be maintained even with a higher PCBM loading ratio. The retransformed phenomenon indicates that the solvent vapor annealing approach is successfully employed in the BCP/fullerene composite.

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Fig. 3 AFM (a–c) height images, TEM (d–f) plane view images and schematic illustrations of L2:PCBM composite thin films with solvent annealing under exposure to saturated methanol vapor for 2 days. The content of PCBM: (a,d) 0%, (b,e) 5% and (c,f) 10%.
polymer chains when the content of PCBM was 10%. Such a phenomenon accounts for the disappearance of the higher order diffraction peaks.

As can be seen in Fig. 4(b), the L2 thin film on the SiOx/Si substrate only reveals a principal Bragg peak. In the previous section, the AFM and TEM images suggest that the L2 thin film is comprised of parallelly-oriented PS nanocylinders within a P4VP matrix. Therefore, one or two monolayers of the parallelly-oriented PS nanocylinders formed within the L2 thin film and contribute to the principal Bragg peak.52,53 As a result, the diffraction position is directly associated with an intercylinder spacing. In comparison with the L2 thin film, such a Bragg peak for the PCBM:L2 thin film presents the variance in the q0 position and diffraction intensity. Similar to the case of the PCBM:L1 composite film, the variances in the diffraction feature of the GISAXS is due to the selective swelling of the P4VP domains by PCBM.

3.4 Memory device characteristics

The electrical behaviors were tested by the current–voltage (I–V) characteristics on the ITO/polymer composite/Al sandwiched architecture with sweeping steps of 0.1 V. The thickness of the self-assembled L1 or L2:PCBM thin films with or without solvent annealing was adjusted to be 60–70 nm with a smooth surface. The loading ratio of the PCBM content in the blend solutions includes 0%, 3%, 5%, 7%, and 10%. Furthermore, each device also contained 25 cells for the statistical test of the electrical characterization. Here, the effects of PCBM content, BCP composition and solvent annealing conditions on the memory device performance were systematically characterized and analyzed.

The electrical properties and switching behaviors of the L1:PCBM composite thin films after solvent annealing are illustrated by the I–V measurements shown in Fig. 5. The pure L1 based device exhibits only a low current in the range of 10−13–10−10 A with the external applied voltage due to its insulating nature. As the loading ratio of PCBM increases, the electronic transition starts to go from insulating to the memory switching and conducting state gradually. By incorporation of a small amount of PCBM (3%) into the L1 matrix, no obvious changes occur in the electronic properties, except for a slightly higher current response as compared to the pure L1 device and only a few memory cells show the switching transition in the electrical properties. However, most of the 5% PCBM composite memory cells exhibit distinctly bistable electrical resistance states. The complete electrical scans of the device based on the 5% PCBM:L1 thin film after annealing are demonstrated through three voltage sequences: 0 to −4.0 V (sweep 1); 0 to −4.0 V (sweep 2); 0 to 4.0 V (sweep 3). Initially the as-fabricated device is in the OFF state (“0” signal in data storage) with a current in the range of 10−12–10−10 A as the voltage sweeps from 0 to −1.5 V progressively. When the voltage increases further, an abrupt current jump occurs at a threshold voltage of −1.5 V, indicating that the dramatic transition from the low-conductivity OFF state to the high-conductivity ON state (“1” signal in data storage). This electronic transition in the first sweep serves as the “writing” process. The current is maintained in the ON state for the subsequent negative sweep from 0 to −4.0 V (sweep 2) and positive sweep from 0 to 4.0 V (sweep 3). This device can not return to the OFF state even after turning off the power (at least 1 h) or applying a reverse bias, indicating a non-volatile write-once-read-many-times (WORM) memory behavior with a high ON/OFF current ratio of 106 at −1.0 V. The stability of the memory device against switching time with a constant voltage

![Fig. 4 GISAXS diffraction profiles](image)

![Fig. 5 I–V characteristics](image)
bias for long periods or continuous pulse voltage stress with a pulse period of 2 μs and a pulse width of 1 μs was also evaluated under the same conditions. Both ON and OFF state currents of the WORM device can be sustained for up to 2.8 h and 10⁸ continuous read pulses of −1 V, as shown in Fig. S5 of ESI†. From this result of excellent stability of the WORM memory device, the archive is supposed to be reliably kept and permanently stored. Besides, some of the device cells with the 7% PCBM content also show a similar bistable WORM switching behavior and others exhibit the conductor behavior. However, the 10% PCBM:L1 device exhibits only a single conduction characteristic due to the high conductivity of the large PCBM content.

In the case of the PCBM:L2 device, a similar trend in the electrical properties changes from insulator to memory switching and conductor as the PCBM content increases from 0% to 10%, as shown in Fig. 6 and Fig. S6 of ESI†. The consecutive sweep cycles of the 5% PCBM:L2 device after solvent annealing have demonstrated bistable conductivity states (Fig. 6). As a negative bias voltage is applied, the current of the memory cell suddenly increases to the higher conductance state at a threshold voltage of −2.7 V with an ON/OFF current ratio of 10⁸. After reading the ON state in the negative sweep (sweep 2), the subsequent application of the third sweep is conducted after turning off the power for about 5 min. It can be reprogrammed from the OFF state to the ON state when the voltage bias increases to −2.6 V (sweep 3) and the current is also kept in the ON state (sweep 4). For the following repeated sweep cycle, the I–V curves suggest that the ON state can be maintained for an approximate period of 5–8 min after the removal of power and would gradually relax back to the OFF state. Therefore, the short retention ability based on the electrical switching concludes the volatile nature of a static random access memory (SRAM) on most of the ITO/5% PCBM:L2/Al cells after solvent annealing. In addition, the unstable ON state of the volatile memory device can be also retained by a refreshing voltage pulse of 1.0 V with a 1 ms duration every 30 s (named as refresh trace). The long-term stability and endurance are also evaluated from the retention time test under the voltage stress and stimulus effect of the read pulse, as shown in Fig. S7 of ESI†. Note that the existence of the PCBM molecules in polymer matrix obviously determines the electrical properties both in the PCBM:L1 and PCBM:L2 polymer composite device.

To classify the solvent annealing effect of the composite thin film on the switching performance, the statistical data including the threshold voltage (Fig. 7 and Fig. S8 of ESI†), the switching probability (Fig. S9 and S10 of ESI†) and the ON or OFF current (Fig. 8 and Fig. S11 of ESI†) extracted from the randomly selected 25 cells in the single memory device were analyzed in detail. For the PCBM:L1 composite film without solvent annealing, the 1% (3% PCBM) and 12 device cells (5% PCBM) devices are able to switch to the ON state as a WORM-like memory device, and the others just work as insulators (Fig. S9 of ESI†). The operation of switching on the bistable WORM cells of the 7% PCBM:L1 device increases to 16 cells, and others almost exhibit a conducting behavior. However, the device based on the 10% PCBM only displays a high conductance state without any significant switching effect. The electronic behaviors of the PCBM:L1 composite films with solvent annealing were also investigated. There are 5, 22, and 9 out of 25 cells capable of the switching behavior for the devices containing 3%, 5% and 7% PCBM (Fig. S9 of ESI†). Of the devices containing the PCBM composites, it is found that the devices with solvent annealing have significantly improved switchable cells compared to the non-annealed ones. Among all, the 5% PCBM:L1 device with solvent annealing is found to exhibit noticeable switching WORM memory behavior. Similar switching phenomena were observed in the PCBM:L2 composite device (Fig. S10 of ESI†) although it exhibits a volatile switching behavior. In addition, the excellent switching performance is based on the annealed 5% PCBM:L2 composite device with 22 switchable cells out of total 25 cells. Apparent electrical transition and better memory switching effect are expected for composite thin films with solvent annealing under enough numbers of statistical data. Again, the PCBM molecules directly play an important role in the operation of the fabricated composite memory device.

![Fig. 6 I–V characteristics of the ITO/5% PCBM:L2/Al device with solvent annealing.](image1)

![Fig. 7 Threshold voltage distributions of the ITO/5% PCBM:L1 or L2/Al device with and without solvent annealing.](image2)
Furthermore, the statistical variation of the threshold voltage and the current distribution of the ON and OFF states in each switchable device obtained from the voltage sweep of the individual cells are considered to demonstrate the switching condition systematically. For the devices without solvent annealing, a relatively wide distribution of the threshold voltage can be observed in both the PCBM:L1 and L2 composite devices (blue bar in Fig. 7 and Fig. S8 of ESI†). For example, the threshold voltage of 5% PCBM:L1 is located in the voltage range of −1.8 to −3.5 V. The variation problem is due to the non-uniform distribution of the aggregated PCBM in the vertical direction. However, for the devices with solvent annealing (red bar in Fig. 7 and Fig. S8 of ESI†), the threshold voltage of the switching device after solvent annealing has a narrower distribution than that containing the same PCBM content without annealing (especially for 5% PCBM), indicating that the individual cells can be switched more uniformly. Besides, the turn-ON voltage would obviously be decreased as the PCBM concentration increases. The minor fluctuation in the electrical properties such as from moisture and oxidation should be excluded since all the devices are prepared and measured inside the glove box. Therefore, the threshold voltage should be strongly associated with the PCBM additive loading and dispersion as well.

Fig. 8 shows the effect of the PCBM content of the L1 composite on the OFF and ON current distribution of the operating cells (read at 1 V). It shows that the ON current is almost located in the similar range of 10⁻⁵–10⁻⁴ A and not sensitive to the PCBM content. However, a higher OFF current can be observed if more PCBM is added into the composite thin film. As a result, 3%, 5% and 7% PCBM devices, with solvent annealing, exhibit an ON/OFF ratio of about 10⁶, 10⁵–10⁶ and 10⁴, respectively, and thus the device with a smaller PCBM content has a higher ON/OFF ratio. In addition, the OFF state of the device without solvent annealing is distributed over a wider current range than that of the device after annealing, probably due to the dispersed morphology of the PCBM molecules. From the analysis of the I–V experiments, it can be seen that as the PCBM loading ratio gets higher, the threshold voltage decreases and the OFF state current increases. Among the tested devices, 5% PCBM:L1 or the L2 composite device, with solvent annealing, shows excellent memory performance with the highest yields on switching operation, narrow distribution on threshold voltages and reasonable separation between the ON and OFF states for a low-misleading rate. Suitable PCBM content and solvent vapor-induced self-assembled morphology can be recognized to apparently form the memory switching channel.

3.5 Mechanism of memory switching behavior

The obtained I–V characteristics for the 5% PCBM:L1 WORM and PCBM:L2 SRAM devices were further analyzed by fitting appropriate conductance models, as shown in Fig. 9 and Fig. S12 (ESI†). With a device configuration of ITO/BCP:PCBM/Al, large band gap PS-b-P4VP BCP may act as carrier blocking moieties and the low-lying LUMO of the PCBM compared to the BCP, can serve as the electron trapping sites embedded in the polymer matrix. The schematic diagrams of the electronic structures corresponding to the operating mechanism are shown...
in Fig. S13 of ESI.† When the negative voltage is applied, electrons are injected into the composite film, trapped by PCBM and then escape from the isolated PCBM trapping islands through a hopping process if sufficient energy can be acquired. At low voltages (from 0 to 0.4 V in first sweep), the current increases linearly with the increase of the voltage bias, indicating that the current response is dominated by an Ohmic model. This is attributed to the thermally generated free electrons at the interface or in the composite layer from the electron injection. With a continuous increase of the voltage bias, the current shows that the voltage square dependence for the voltage between 0.4 V to the threshold voltage is approximately correlated to the space-charge-limited-current (SCLC).54,55 The space-charge accumulates with an increasing voltage (>0.4 V), and is controlled through thermally active hopping conductions. The power-law relationship is quite similar to the charge-transport model of quantum dots or nanoparticles, or conjugated polymers separated by the tunneling barriers.18,56,57 After exceeding the threshold voltage (achievement of the writing process), the injected electrons from the Al electrode can possibly capture and fill the trapping sites. The electrons are able to flow effectively and continuously in the composite film and the current increases sharply around six orders of magnitude to a high conductive state. The electrical switching behavior results from the efficient hopping of charge carriers in the neighboring PCBM molecules. In the ON state, after electrical switching, the plot of \( \log(I) \) versus \( V^{1/2} \) shows a linear characteristic (Fig. 9(b)). It suggests that the Poole–Frenkel emission is the dominant charge-transport process of the ON state, with electrically charged defects through the bulk materials.27

The loading ratio of PCBM in the BCP matrix allows the device to manipulate the threshold voltage and ON/OFF current ratio, since the separation of the PCBM domain can be controlled through the PCBM distribution in the microphase-separated BCP matrix.28,29 Only a few device cells can be switched in the 3% PCBM:L1 or L2 composite due to the large separation between PCBM domains, which prevents the charge carriers from hopping. However, the 5% or 7% PCBM composite devices exhibit smaller threshold voltages and ON/OFF ratios (higher OFF state current) than the 3% device. The decrease in distance between the isolated PCBM molecules can easily overcome the percolation threshold of the trapping sites for the effective charge carrier transport. The energy barrier for charge-transport decreases with the increase in the PCBM content of the composites, and more charges in the memory layer can be captured by PCBM molecules. For the device containing 10% PCBM, the close stacking PCBM can form a continuous network in the active layer. It may lead to electrically effective vertical-connected channels (ohmic conduction) between the top and bottom electrodes from the aggregated PCBM molecules and short-circuit the devices directly.

The self-assembled nanostructures of the L1 and L2 composite systems, with or without solvent annealing, have a great influence on the memory performances, such as memory switching percentage, threshold voltage distribution and volatility of the data storage. Optimized PCBM distribution is especially an important factor for obtaining two-terminal bistable composite memory devices. For the L1 composite system, the non-annealed morphology is based on dimple-like micelles and the annealed nanostructure shows perpendicular cylinders with a P4VP:PCBM core and a PS corona in a hexagonal array. For the PCBM:L2 composite system, the non-annealed film only shows a disorder of the aggregated phases, and horizontal cylinders with a PS core and a P4VP:PCBM corona can be obtained after annealing. The switching characteristic parameters of the memory devices critically depend on the properties of the active memory materials and relative interfaces. Both the L1 and L2 PCBM composite thin films, with solvent annealing, exhibit narrower threshold voltage distributions and higher effective switching percentages than the non-annealed disordered film, indicating that composite films with specific nanostructures obtain well-dispersed PCBM in the P4VP domains to form electron transporting channels, as shown in Fig. 10. Particularly, the volume fraction of each block can be used to tune the different types of charge transport architecture. Also, the inter PCBM spacing also depends on the self-organized BCP at the molecular level. Therefore, the results of the high bistability effects could be achieved in terms of well distribution on functional molecules guided by the ordered BCP host matrix. It is worthy to note that the distinct difference in threshold voltage and volatility of the data storage between L1 and L2:PCBM annealed composite films also arise from the morphology effect. Due to the interaction between PCBM and pyridine, PCBM molecules are mainly dispersed in the core of L1 and corona of L2, and the longer P4VP block in L2 gives a larger proportion of the P4VP region in the thin film. Thus, the distance between the neighboring PCBM domains in L1 is probably much closer than L2 for the same PCBM content. As a result, the L2:PCBM film needs to obtain a stronger external electric field for hopping across the insulating matrix and charging the PCBM molecules. Thus, the average threshold voltage of the L1:PCBM device is slightly smaller than for the L2:PCBM device. The deep trapping

![Fig. 10](https://example.com/fig10.png) Charge injection and transport behaviors in the (a) ITO/PCBM:L1/Al device and (b) the ITO/PCBM:L2/Al device after thermal annealing.
site from the LUMO level between L1 and PCBM may not easily be emitted from by captured electrons in PCBM even through turning OFF the power or reversing the field, and the high conductance state is retained without returning to the OFF state in the observed WORM L1:PCBM device. Furthermore, for the L2:PCBM device, PCBM has more opportunities to contact with the electrode originally from the PCBM domain located on the outer side of the horizontal cylinder. The probability of the charges being able to escape is typically determined by the electrode/organic layer interface. When the power is turned off, the trapped electrons can be extracted from the PCBM to the electrode and the system returns to the original OFF state leading to the volatile nature of the L2:PCBM memory device. The remnant ON states (but still volatile) of the L2:PCBM device may be due to the delayed transfer of the trapped charge that occurs due to the L2:PCBM large trapping barriers.

In addition, the convincing switching behavior of the composite film is strongly based on the morphology- and PCBM concentration-dependent electronic properties, which rules out the possibility of an electronic transition from metal filament formation. The difference in the spatially distributed PCBM molecules has a strong relationship to the device performance. Therefore, it concludes that the electrical switching behavior is attributed to the electric field-induced space-charge formation and intrinsic traps of the composite layer in the sandwiched memory device. Stored information in discrete and specific charge trapping sites after the solvent annealing process can improve the device reliability. Controlling the physical interaction and orientation of the BCP composites offer the practical approach to make thin film morphologies with tunable nanostructures for advanced device applications.

4. Conclusions

We have demonstrated tunable and controllable electrical switching behavior through utilizing self-assembled PCBM:PS-b-P4VP composite films with a simple sandwich device architecture. The effects of interaction between the PCBM and PS-b-P4VP and the reorientation force led to specific thin film morphologies for electrical memory device as evinced by AFM, TEM, and GISAXS. The I–V characteristics for the PCBM:PS-b-P4VP composite device showed the electrically insulating, bistable switching or conducting effect and a strong dependence on the loading of the PCBM, block length ratio and solvent annealing conditions. Also, the memory characteristics could be controlled by the morphology of the BCP composite thin film using a different block ratio or solvent annealing. The L1:PCBM memory device showed a non-volatile WORM-like memory behavior but the L2:PCBM device exhibited a volatile SRAM nature. The spatial distributions of the PCBM domains and the ability of the charges to escape or be captured in the trapping environment determined the memory characteristics. The technique allowed for the improvement of statistical threshold voltages and ON/OFF ratios with more well-defined structures after solvent annealing. The switching mechanism, based on experimental and the fitted I–V curves, was explained by the space-charge-limited-current in the OFF state and the charge-transport-dominated Poole–Frenkel model in the ON state. Electric field induced conductance transition depended on the efficient charge transport ability with the co-existing charge-trapping environment in the composite thin film. These results demonstrated the morphology-controllable charge-trapping nanostructures for undergoing memory bistable states.

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